

STBV42D

High voltage fast-switching NPN power transistor

Preliminary data

Features

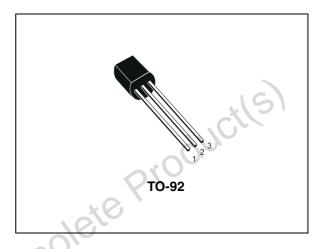
- High voltage capability
- Low spread of dynamic parameters
- Very high switching speed
- Integrated free-wheeling diode

Application

Compact fluorescent lamps (CFLs)

Description

The device is manufactured using high voltage multi epitaxial planar technology for high switching speeds and high voltage capability. It uses a cellular emitter structure with planar edge termination to enhance switching speeds while maintaining the wide RBSOA.



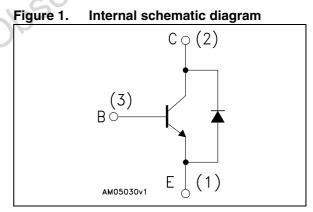


Table 1. Device summary

Order code	Marking	Package	Packaging	
STBV42D	BV42D	TO-92	BAG	

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This is preliminary information on a new product now in development or undergoing evaluation. Details are subject to change without notice.

Electrical ratings 1

Table 2. Absolute maximum ratings

Symbol	Parameter	Value	Unit		
V _{CES}	Collector-emitter voltage (V _{BE} = 0)	700	V		
V _{CEO}	Collector-emitter voltage $(I_B = 0)$	400	V		
V_{EBO}	Collector-base voltage ($I_C = 0$)	9	V		
Ι _C	Collector current	1	A		
I _{CM}	Collector peak current (t _P < 5 ms)	2	Α		
Ι _Β	Base current	0.5	JSA		
I _{BM}	Base peak current (t _P < 5 ms)	1	A		
P _{TOT}	Total dissipation at $T_c = 25 \ ^{\circ}C$	- CV	W		
T _{STG}	Storage temperature	- 65 to 150	°C		
TJ	Max. operating junction temperature	150			
Table 3.	Thermal data	lete			

Table 3. Thermal data

	Symbol	Parameter	Value	Unit
	R _{thJC}	Thermal resistance junction-case	125	°C/W
obsole				



2 Electrical characteristics

 T_{case} = 25 °C; unless otherwise specified.

Symbol	Parameter	Test o	conditions	Min.	Тур.	Max.	Unit
I _{CES}	Collector cut-off current $(V_{BE} = 0)$	V _{CE} = 700 V V _{CE} = 700 V	T _C = 125 °C			1 5	mA mA
I _{EBO}	I _{EBO} Emitter cut-off current (I _C = 0)					1	mA
V _{CEO(sus)}	Collector-emitter sustaining voltage (I _B = 0)	I _C = 1 mA		400	~	S	v
V _{CE(sat)} ⁽¹⁾	Collector-emitter saturation voltage	I _C = 0.25 A I _C = 0.5 A I _C = 0.75 A	l _B = 125 mA	99	0.2 0.3 0.4	0.5 1 1.5	V V V
V _{BE(sat)} ⁽¹⁾	Base-emitter saturation voltage	I _C = 0.25 A I _C = 0.5 A	I _B = 50 mA I _B = 125 mA			1 1.2	V V
h _{FE} ⁽¹⁾	DC current gain	$I_{C} = 5 \text{ mA},$ $I_{C} = 0.4 \text{ A},$ $I_{C} = 0.8 \text{ A}$	V _{CE} = 2 V V _{CE} = 5 V V _{CE} = 5 V	12 10 5		30 20	
t _f	Inductive Load Fall time	$I_{C} = 0.25 \text{ A}$ $I_{B(on)} = -I_{B(off}$ L = 3 mH	V _{clamp} = 300 V) = 50 mA <i>Figure 2</i>		0.3		μs
V _F	Diode forward voltage	I _F = 350 mA				1.7	V

 Table 4.
 Electrical characteristics

1. Pulse test: pulse duration ≤ 300 μs, duty cycle ≤ 2 %



2.1 Test circuit

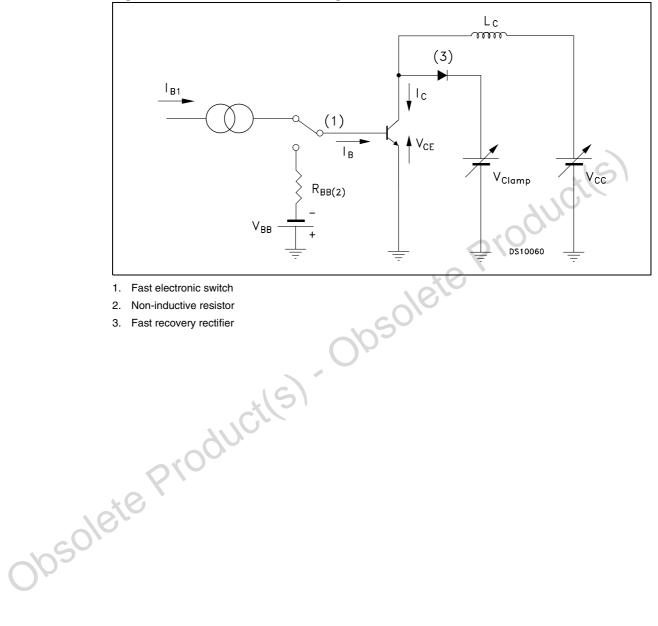


Figure 2. Inductive load switching test circuit

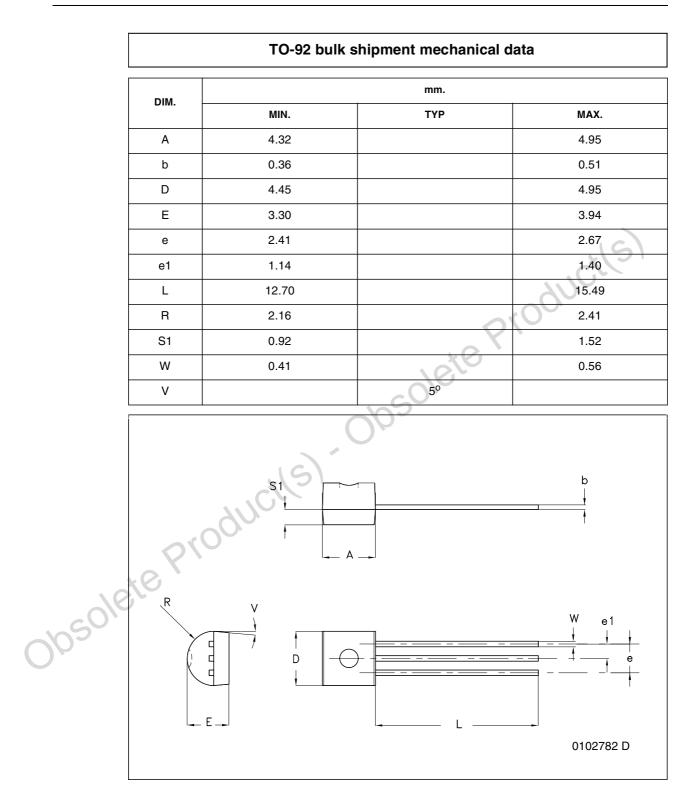


3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

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obsolete Product(s). Obsolete Product(s)





4 Revision history

Table 5.Document revision history

Date	Revision	Changes
08-Mar-2010	1	First release.
28-Apr-2010	2	Inserted V _F maximum value Table 4 on page 3.

obsolete Product(s). Obsolete Product(s)



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